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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/764,484	01/27/2004	Atsuhiro Mori	61282-059	6279
7590 10/01/2008 MCDERMOTT, WILL & EMERY 600 13th Street, N.W. Washington, DC 20005-3096			EXAMINER	
			ELAND, SHAWN	
			ART UNIT	PAPER NUMBER
			2188	
			MAIL DATE	DELIVERY MODE
			10/01/2008	PAPER

**Please find below and/or attached an Office communication concerning this application or proceeding.**

The time period for reply, if any, is set in the attached communication.

<b>Office Action Summary</b>	<b>Application No.</b> 10/764,484	<b>Applicant(s)</b> MORI, ATSUHIRO
	<b>Examiner</b> SHAWN ELAND	<b>Art Unit</b> 2188

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --  
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If no period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED. (35 U.S.C. § 133).

Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

#### Status

1) Responsive to communication(s) filed on 21 July 2008.

2a) This action is FINAL.      2b) This action is non-final.

3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

#### Disposition of Claims

4) Claim(s) 1-7 is/are pending in the application.

4a) Of the above claim(s)       is/are withdrawn from consideration.

5) Claim(s) 2-4,6 and 7 is/are allowed.

6) Claim(s) 1 and 5 is/are rejected.

7) Claim(s)       is/are objected to.

8) Claim(s)       are subject to restriction and/or election requirement.

#### Application Papers

9) The specification is objected to by the Examiner.

10) The drawing(s) filed on       is/are: a) accepted or b) objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

#### Priority under 35 U.S.C. § 119

12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a) All    b) Some \* c) None of:

1. Certified copies of the priority documents have been received.
2. Certified copies of the priority documents have been received in Application No.      .
3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

#### Attachment(s)

1) Notice of References Cited (PTO-892)

2) Notice of Draftsperson's Patent Drawing Review (PTO-948)

3) Information Disclosure Statement(s) (PTO/0250) Paper No(s)/Mail Date 06/16/08

4) Interview Summary (PTO-413) Paper No(s)/Mail Date      

5) Notice of Informal Patent Application

6) Other:

**DETAILED ACTION**

A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR 1.114. Applicant's submission filed on 07/21/08 has been entered.

*Status of Claims*

Claims 1 – 7 are pending in the Application.

Claims 1 – 7 have been amended.

Claims 1 & 5 are rejected.

Claims 2 – 4, & 6 – 7 are allowed.

***Claim Rejections - 35 USC § 112***

The following is a quotation of the first paragraph of 35 U.S.C. 112:

The specification shall contain a written description of the invention, and of the manner and process of making and using it, in such full, clear, concise, and exact terms as to enable any person skilled in the art to which it pertains, or with which it is most nearly connected, to make and use the same and shall set forth the best mode contemplated by the inventor of carrying out his invention.

Claims 1 & 5 are rejected under 35 U.S.C. 112, first paragraph, as failing to comply with the written description requirement. The claim(s) contains subject matter which was not described in the specification in such a way as to reasonably convey to one skilled in the relevant art that the inventor(s), at the time the application was filed, had possession of the claimed invention.

Claims 1 & 5 disclose "generating a normal clock signal to be supplied to the first and second data input/output devices" at lines 5 – 7 for claim 1 and lines 4 – 5 for claim 5. According to figure 1 and Applicant's written specification (page 10, lines 10 - 11 of the "DESCRIPTION OF THE PREFERRED EMBODIMENT" section the clock generating device 104 supplies a clock signal to the second data input/output device 103. There is no supporting text for sending a clock signal from the clock generating device to the first input/output device 102.

***Allowable Subject Matter***

Claims 2 – 4, & 6 – 7 are allowed.

The following is a statement of reasons for the indication of allowable subject matter:

In regard to claims 2 – 4, the prior art of record does not teach or suggest a clock generating device comprising a clock oscillating section generating a normal clock signal to be supplied to the processor, and a clock wait control section generating a wait clock signal having a cycle which is integer times as much as the normal clock signal has; and an access arranging device causing the clock oscillating section to stop the normal clock signal for one clock cycle, causing the clock oscillating section to stop the normal clock signal for one clock cycle, causing the clock wait control signal section to supply the wait clock signal to the processor, and carry out access of the input/output control device with a priority when a contention of access of the processor and the input/output control device to the built-in memory is generated.

In regard to claim 6, the prior art of record does not teach or suggest a clock generating device comprising a clock oscillating section generating a normal clock signal to be supplied to the processor, and a clock wait control section generating a wait clock signal having a cycle which is integer times as much as the normal clock signal has, providing the normal clock signal to the processor and the data input/output device; and providing the clock wait signal to cause the normal clock signal for the processor to be stopped for one clock cycle and not allowing the processor to access the memory for one clock cycle when a contention of the access of the data input/output device and the processor to the memory is generated.

In regard to claim 7, the prior art of record does not teach or suggest a clock generating device comprising a clock oscillating section generating a normal clock signal to be supplied to the processor, and a clock wait control section generating a wait clock signal having a cycle which is integer times as much as the normal clock signal has, providing the normal clock signal to the processor and the data input/output device; and providing the clock wait signal to cause the normal clock signal for the processor to be stopped.

***Examiner's Information***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Shawn Eland whose telephone number is (571) 270-1029. The examiner can normally be reached on MO - TH, & every other FR.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Hyung Sough can be reached on (571) 272-4199. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Hyung S. Sough/  
Supervisory Patent Examiner, Art Unit 2188  
09/29/08

/Shawn Eland/  
Examiner, Art Unit 2188  
10/4/2008